FIG. 1

DISPLAY
116

RECEIVER 108
104

RECEIVER 108
114

PROCESSOR RFAG.

2 Volume rendering processor Silos memory 506 8 Sico Thistness setting control Volume scan converter Video processor FIG. 2 US data memory Display 80( 107 Rocaiver 99 7 Scanned Votume Transducer

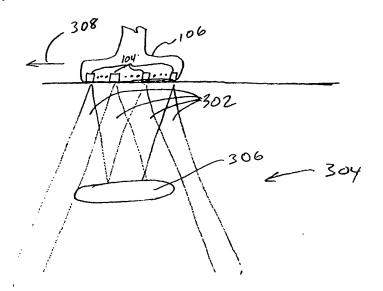


Figure 3

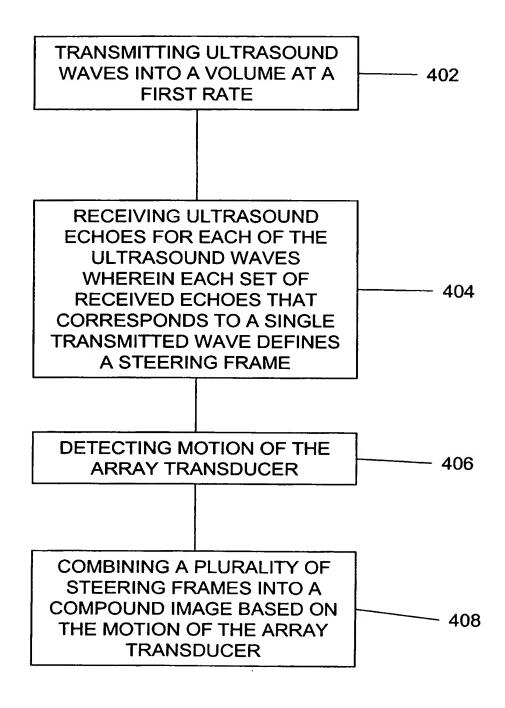


Figure 4